

"Half Bridge" MTP Trench IGBT, 75 A



PRIMARY CHARACTERISTICS							
V _{CES}	1200 V						
$V_{CE(on)}$ typical at $I_C = 40 \text{ A}$	2.24 V						
I_C at $T_C = 25$ °C	75 A						
Speed	8 kHz to 30 kHz						
Package	MTP						
Circuit configuration	Half bridge						

FEATURES

- Trench gate field stop technology
- Positive V_{CE(on)} temperature coefficient
- 5 µs short circuit capability
- Square RBSOA
- HEXFRED® antiparallel diodes with ultrasoft reverse recovery and low V_F
- Al₂O₃ DBC
- Very low stray inductance design for high speed operation
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

BENEFITS

- · Optimized for welding, UPS and SMPS applications
- Rugged with ultrafast performance
- Benchmark efficiency above 20 kHz
- · Outstanding ZVS and hard switching operation
- Low EMI, requires less snubbing
- Excellent current sharing in parallel operation
- · Direct mounting to heatsink
- PCB solderable terminals
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{CES}		1200	V	
Continuous collector current		T _C = 25 °C	75		
Continuous collector current	I _C	T _C = 102 °C	40		
Pulsed collector current	I _{CM}	$T_J = 150 ^{\circ}\text{C}, t_p = 6 \text{ms}, V_{GE} = 15 \text{V}$	150	A	
Clamped inductive load current	I _{LM}		120		
Diode continuous forward current	I _F	T _C = 105 °C	21		
Diode maximum forward current	I _{FM}		160]	
Gate to emitter voltage	V_{GE}		± 20	V	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500]	
Maximum payer dissipation (only ICBT)	В	T _C = 25 °C	305	W	
Maximum power dissipation (only IGBT)	P_D	T _C = 100 °C	122	VV	





ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Collector to emitter breakdown voltage	V _{(BR)CES}	V _{GE} = 0 V, I _C = 2 mA	1200	-	-	V
		V _{GE} = 15 V, I _C = 40 A	-	2.24	2.65	
Callegates to emittee estimation valtage	V	$V_{GE} = 15 \text{ V}, I_{C} = 80 \text{ A}$	-	2.84	-	V
Collector to emitter saturation voltage	$V_{CE(on)}$	V _{GE} = 15 V, I _C = 40 A, T _J = 150 °C	-	2.53	-	
		V _{GE} = 15 V, I _C = 80 A, T _J = 150 °C	-	3.44	-	
Gate threshold voltage	$V_{GE(th)}$	$V_{CE} = V_{GE}$, $I_C = 2 \text{ mA}$	4.6	5.9	7.6	
Temperature coefficient of threshold voltage	$V_{GE(th)}/\Delta T_J$	V _{CE} = V _{GE} , I _C = 2 mA (25 °C to 125 °C)	-	-13	-	mV/°C
Transconductance	9 _{fe}	$V_{CE} = 50 \text{ V}, I_{C} = 40 \text{ A}$	-	29	-	S
Zero gate voltage collector current		V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 25 °C	-	0.6	50	μA
	I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 125 °C	-	0.31	-	mΛ
		V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 150 °C	-	1.16	-	mA
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	±250	nA

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Q_g	I _C = 40 A	-	158	-	
Gate to emitter charge (turn-on)	Q _{ge}	V _{CC} = 960 V	-	17	-	nC
Gate to collector charge (turn-on)	Q_{gc}	V _{GE} = 15 V	-	85	-	
Turn-on switching loss	E _{on}	$V_{CC} = 600 \text{ V}, I_{C} = 40 \text{ A}, V_{GE} = 15 \text{ V},$	-	0.76	-	
Turn-off switching loss	E _{off}	R _g = 5 Ω , L = 200 μ H, T _J = 25 °C, energy losses include tail and diode	-	1.14	-	
Total switching loss	E _{tot}	reverse recovery	-	1.9	-	1
Turn-on switching loss	E _{on}	V_{CC} = 600 V, I_{C} = 40 A, V_{GE} = 15 V, R_{g} = 5 Ω, L = 200 μH, T_{J} = 125 °C, energy losses include tail and diode reverse recovery	-	1.02	-	mJ
Turn-off switching loss	E _{off}		-	1.83	-	
Total switching loss	E _{tot}		-	2.85	-	
Input capacitance	C _{ies}	V _{GF} = 0 V	-	3200	-	
Output capacitance	C _{oes}	V _{CC} = 25 V	-	220	-	рF
Reverse transfer capacitance	C _{res}	f = 1.0 MHz	-	80	-	
Reverse bias safe operating area	RBSOA	$T_J = 150 ^{\circ}\text{C}, I_C = 120 \text{A}$ $V_{CC} = 800 \text{V}, V_p = 1200 \text{V}$ $R_g = 10 \Omega, V_{GE} = + 15 \text{V} \text{ to } 0 \text{V}$	Fullsquare			
Short circuit safe operating area	SCSOA	$T_J = 150 ^{\circ}\text{C},$ $V_{CC} = 600 \text{V}, V_p = 1200 \text{V}$ $V_{GE} = + 15 \text{V} \text{to} 0 \text{V}$	5	-	-	μs

DIODE SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
		I _C = 40 A	-	2.98	3.38	
Diode forward voltage drop V _{FM}		I _C = 80 A	- 3.90 - 3.08	-		
	V_{FM}	I _C = 40 A, T _J = 125 °C		3.08	-	V
	I _C = 80 A, T _J = 125 °C	-	4.29	-		
		I _C = 40 A, T _J = 150 °C	-	3.12	-	
Reverse recovery energy of the diode	E _{rec}	$V_{GE} = 15 \text{ V}, R_{\alpha} = 5 \Omega, L = 200 \mu\text{H}$	-	574	-	μJ
Diode reverse recovery time	t _{rr}	$V_{CC} = 600 \text{ V}, I_{C} = 40 \text{ A}$	-	120	-	ns
Peak reverse recovery current	I _{rr}	T _J = 125 °C	-	43	-	Α



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temper range	rature	T _J , T _{Stg}		-40	-	150	°C
Junction to case	IGBT	р		-	-	0.41	
Junction to case	Diode	R _{thJC}		-	-	0.61	°C/W
Case to sink per module		R _{thCS}		-	0.06	-	
Clearance (1)			External shortest distance in air between 2 terminals	5.5	-	-	
Creepage ⁽²⁾			Shortest distance along external surface of the insulating material between 2 terminals	8	-	-	mm
Mounting torque to heatsink			A mounting compound is recommended and the torque should be checked after 3 hours to allow for the spread of the compound. Lubricated threads.	3 ± 10 %		Nm	
Weight					66		g

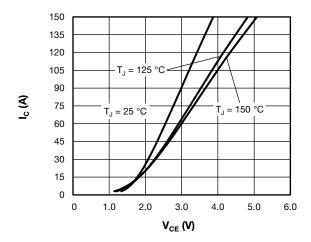


Fig. 1 - Typical Trench IGBT Output Characteristics, $V_{\text{GE}} = 15 \text{ V}$

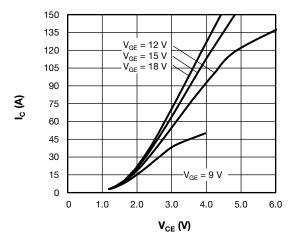


Fig. 2 - Typical Trench IGBT Output Characteristics, T_J = 125 $^{\circ}$ C

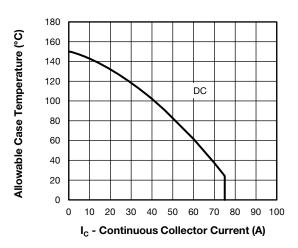


Fig. 3 - Maximum Trench IGBT Continuous Collector Current vs.

Case Temperature

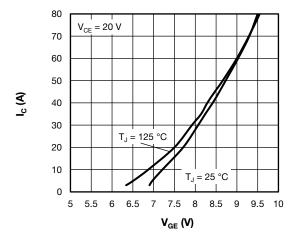


Fig. 4 - Typical Trench IGBT Transfer Characteristics

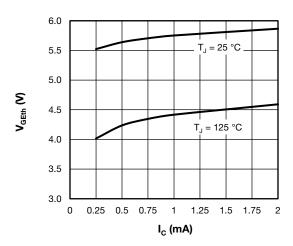


Fig. 5 - Typical Trench IGBT Gate Threshold Voltage

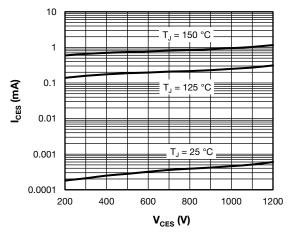


Fig. 6 - Typical Trench IGBT Zero Gate Voltage Collector Current

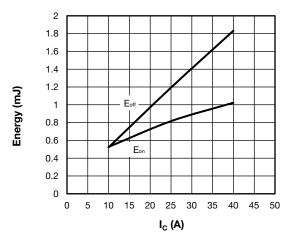


Fig. 7 - Typical Trench IGBT Energy Loss vs. I_C (with Antiparallel Diode) $T_J = 125~^{\circ}C, \, V_{CC} = 600~V, \, R_g = 4.7~\Omega, \, V_{GE} = +15V/-15V, \, L = 500~\mu H$

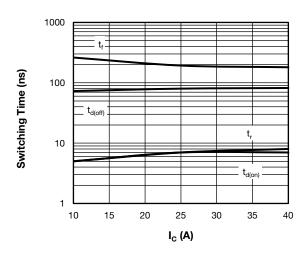


Fig. 8 - Typical Trench IGBT Switching Time vs. I_C (with Antiparallel Diode) I_J = 125 °C, I_C = 600 V, I_C = 4.7 I_C I_C +15V/-15V, I_C = 500 I_C I_C

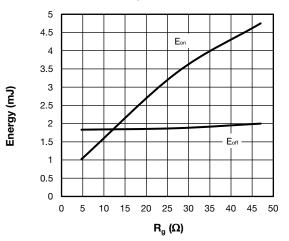


Fig. 9 - Typical Trench IGBT Energy Loss vs. R_g (with Antiparallel Diode) T_J = 125 °C, V_{CC} = 600 V, I_C = 40 A, V_{GE} = +15V/-15V, L = 500 μH

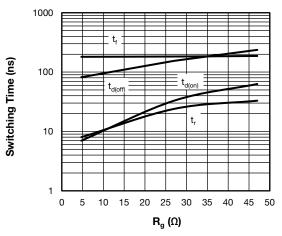


Fig. 10 - Typical Trench IGBT Switching Time vs. R_g (with Antiparallel Diode) T_J = 125 °C, V_{CC} = 600 V, I_C = 40 A, V_{GE} = +15V/-15V, L = 500 μ H

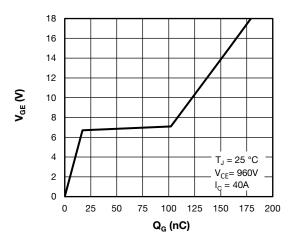


Fig. 11 - Typical Trench IGBT Gate Charge vs. Gate to Emitter Voltage

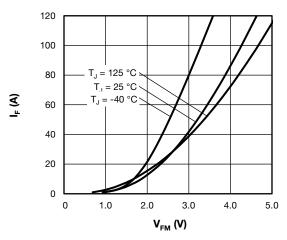


Fig. 12 - Typical Diode Forward Characteristics

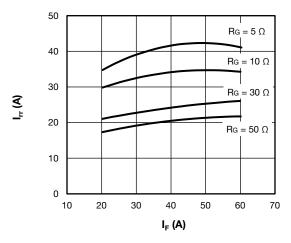


Fig. 13 - Typical Diode I_{rr} vs. I_{F} , $I_{J} = 125 \, ^{\circ}C$

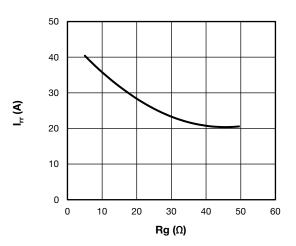


Fig. 14 - Typical Diode I_{rr} vs. R_g $T_J = 125$ °C; $I_F = 40$ A

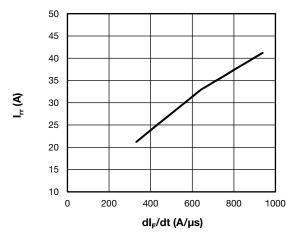


Fig. 15 - Typical Diode I_{rr} vs. dI_F/dt V_{CC} = 600 V; V_{GE} = 15 V; I_{CE} = 40 A; T_J = 125 °C

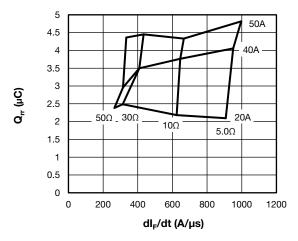


Fig. 16 - Typical Diode Q_{rr} vs. dI_F/dt $V_{CC} = 600$ V; $V_{GE} = 15$ V; $T_J = 125$ °C

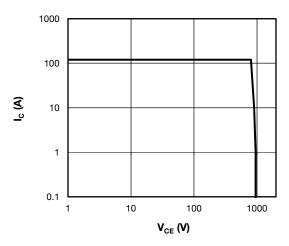


Fig. 17 - Trench IGBT Reverse BIAS SOA T_J = 150 °C, I_C = 120 A, R_g = 10 $\Omega,$ V_{GE} = +15V / 0V, V_{CC} = 800 V, V_p = 1200 V

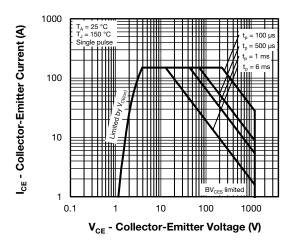


Fig. 18 - Trench IGBT Safe Operating Area

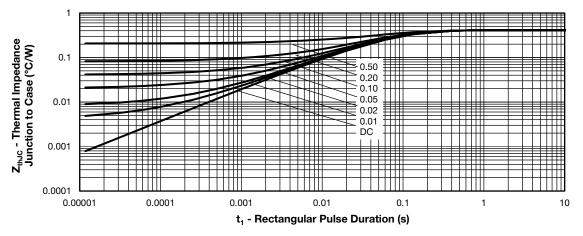


Fig. 19 - Maximum Trench IGBT Thermal Impedance Z_{thJC} Characteristics

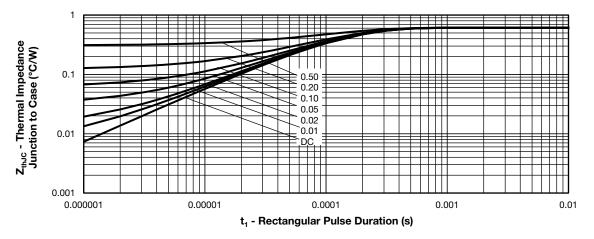
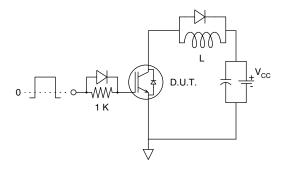


Fig. 20 - Maximum Diode Thermal Impedance Z_{thJC} Characteristics





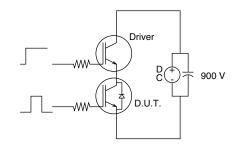


Fig. 21 - Gate Charge Circuit (Turn-Off)

Fig. 23 - S.C. SOA Circuit

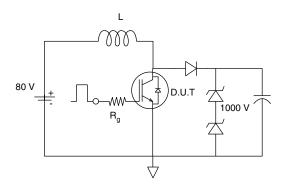


Fig. 22 - RBSOA Circuit

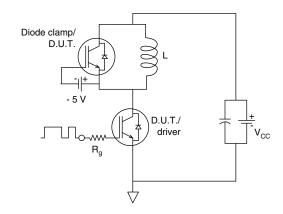
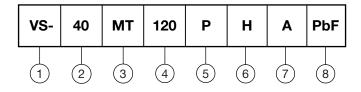


Fig. 24 - Switching Loss Circuit

ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (40 = 40 A)

Essential part number

4 - Voltage code (120 = 1200 V)

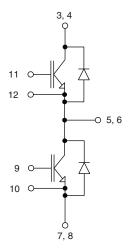
5 - Speed / type (P = trench IGBT)

6 - Circuit configuration (H = half bridge)

7 - $A = Al_2O_3$ DBC substrate

8 - PbF = lead (Pb)-free

CIRCUIT CONFIGURATION

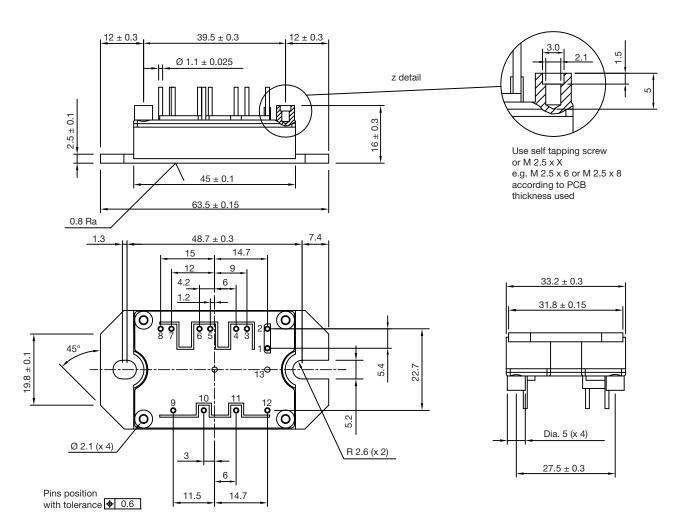


LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95175			



MTP

DIMENSIONS in millimeters



Note

· Unused terminals are not assembled in the package



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