Thick Film Resistor Networks, Dual-In-Line, Molded DIP



- 8 bit, R/2R ladder networks for D/A and A/D converter with bi-polar or CMOS switches
- 0.190" (4.83 mm) maximum seated height
- Rugged, molded case construction
- Thick film resistive elements
- Low temperature coefficient (-55 °C to 125 °C) ± 100 ppm/°C
- Reduces total assembly costs
- Compatible with automatic inserting equipment
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details.

STANDARD ELECTRICAL SPECIFICATIONS											
GLOBAL MODEL	SCHEMATIC	POWER RATING ELEMENT P _{70 °C} W	POWER RATING PACKAGE P _{70 °C} W	RESISTANCE RANGE ⁽¹⁾ Ω	TOLERANCE ± %	TEMPERATURE COEFFICIENT (0 °C to 70 °C) ± ppm/°C	LINEARITY (0 °C to 70 °C)				
T16L	08 R8	0.050	1.8	50 to 1M	2	100	± 0.5 LSB				

Note

⁽¹⁾ 25K, 50K, and 100K are standard, other values available on special order

GLOBAL PART NUMBER INFORMATION									
New Global Part Numbering: T16L08100KTT (preferred part number format)									
T 1	L	0 8 1 0 0 K T T							
GLOBAL MODEL SCHEMATIC T16L 08 R8		ATIC	$\begin{tabular}{ c c c c } \hline RESISTANCE VALUE (R) \\ \hline R = \Omega \\ K = k\Omega \\ M = M\Omega \\ \hline 5K00 = 5 k\Omega \\ \hline 5K10 = 5.1 k\Omega \\ \hline 100K = 100 k\Omega \\ \hline Reference schematic \\ \hline \end{tabular}$		TERMINAL FINISH T = Sn90/Pb10 C = Sn95.5/Ag3.9/Cu0.6		PACKAGING T = tube		
			if R = 5 k Ω , then 2R = 10 k Ω if R = 100 k Ω , then 2R = 200 k Ω						
Historical Part Numberi	ng: T16L08104	S10 (will c	ontinue to be a	ccepted)					
T16L		08			104		S10		
HISTORICAL MODEL N		NUMBEF	OF BITS	RESISTAN	RESISTANCE VALUE (R)		TERMINAL FINISH		

Note

⁽¹⁾ For additional information on packaging, refer to the "Through-Hole Network Packaging" document (<u>www.vishay.com/doc?31542</u>)

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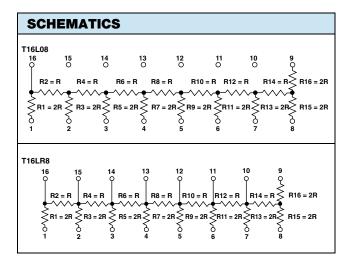
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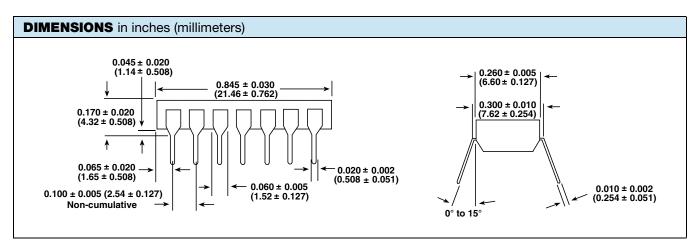
T16L

Vishay Techno



RATIO MATCH TOLERANCE

R1/R2 = 2 % ± 1 %
R1/R3 = 1 % ± 1 %
R1/R4 = 2 % ± 1 %
R1/R5 = 1 % ± 1 %
R1/R6 = 2 % ± 1 %
R1/R7 = 1 % ± 1 %
R1/R8 = 2 % ± 1 %
R9/R10 = 2 % ± 0.5 %
$R11/R12 = 2 \% \pm 0.4 \%$
R15/R13 = 1 % ± 0.2 %
R15/R14 = 2 % ± 0.2 %





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