				0.1, 0.20, 0.0, 1	
		ww.landandmaritime.			x?DocT
working	voltage shall be	e √P x R or maximu	m working voltage,	whichever is less.	
AL SF	PECIFICATI	ONS			
		UNIT		PSF2012	
tion at 8	85 °C	W		0.125	
ent volt	age	V≅		200	
age (1 i	min)	V _{eff}			> 50
ance		K/W		< 1300	

0.1. 0.25. 0.5. 1

MAXIMUM POWER TEMPERATURE RESISTANCE GLOBAL SIZE RATING WORKING VOLTAGE ⁽¹⁾ TOLERANCE COEFFICIENT RANGE **ENCAPSULATION** *P*_{85 °C} W MODEL INCH ± % ± ppm/°C Ω VDC 0.01, 0.02, 0.05, PSF2012 2012 0.125 200 15 to 100K 5, 10, 15, 25 Epoxy 0.1, 0.25, 0.5, 1 0.01, 0.02, 0.05, 15 to 500K PSF4527 4527 0 25 300 5, 10, 15, 25 Thermoplastic 0.1, 0.25, 0.5, 1

Metal Film Resistors, High Precision, High Stability, Surface Mount

Notes

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Marking: Print-marked-model, value, tolerance, TC, date code.
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STANDARD ELECTRICAL SPECIFICATIONS

DSCC has created a drawing to support the need for a precision 2012-sized product. Vishay Dale is listed as a resource on this drawing as

follows:.						
DSCC DRAWING NUMBER	VISHAY DALE MODEL	POWER RATING P _{85 °C} W	RESISTANCE RANGE Ω	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C	MAX. WORKING VOLTAGE ⁽¹⁾ V _{DC}
02001	PSF20121	0.125	15 to 100 K	0.01, 0.02, 0.05,	5, 10	200

This drawing TYPE=DSCCdwq. (1) Continuous w

TECHNICAL SPECIFICA	TIONS

PARAMETER	UNIT	PSF2012	PSF4527
Rated dissipation at 85 °C	W	0.125	0.25
Limiting element voltage	V≅	200	300
Insulation voltage (1 min)	V _{eff}	> 5	00
Thermal resistance	K/W	< 1300	< 520
Insulation resistance	Ω	≥ 1	0 ¹¹
Category temperature range	°C	- 55 to + 150	
Failure rate	10 ⁻⁹ /h	<1	
Weight/1000 pieces (typical)	g	90	760

GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: PSF201220K50BYTA (preferred part number format) κ Ρ s F 2 0 2 2 0 5 0 В Υ т Α 1 GLOBAL MODEL **RESISTANCE VALUE TOLERANCE CODE** TEMP. COEFFICIENT PACKAGING SPECIAL **PSF2012** $\mathbf{R} = \Omega$ $T = \pm 0.01 \%$ $\mathbf{Z} = \pm 5 \text{ ppm/°C}$ EK = Lead (Pb)-free, bulk Blank = Standard **PSF4527** $Q = \pm 0.02 \%$ EA = Lead (Pb)-free, T/R $\mathbf{K} = \mathbf{k}\Omega$ $\mathbf{Y} = \pm 10 \text{ ppm/°C}$ (Dash number) **15R00** = 15 Ω $A = \pm 0.05 \%$ $\mathbf{X} = \pm 15 \text{ ppm/°C}$ BA = Tin/lead, bulk (Up to 2 digits) $B = \pm 0.1 \%$ **1K000** = 1 kΩ $\mathbf{E} = \pm 25 \text{ ppm/°C}$ TA = Tin/lead, T/R (full) From 1 to 99 as **C** = ± 0.25 % **500K0** = 500 kΩ 0 = Special applicable $\mathbf{D} = \pm 0.5 \%$ F = +1%

Note

For additional information on packaging, refer to the Surface Mount Resistor Packaging document (www.vishav.com/doc?31543). ٠

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Vishay Dale

- resistance
- Molded encapsulation
- Wraparound compliant terminations eliminate the risk of solder fillet cracking
- Solderable terminations
- Excellent stability at different environmental conditions
- RoHS For axial-leaded product, see Vishay Dale's PTF datasheet (www.vishay.com/doc?31019) COMPLIANT

low temperature coefficient

 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

Note

FEATURES Extremely

Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.

www.vishay.com





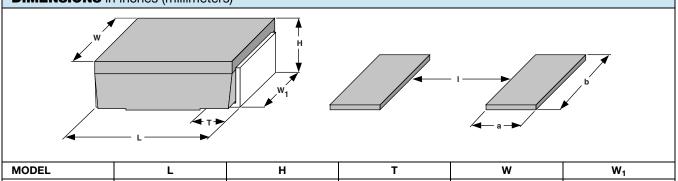
е





Vishay Dale

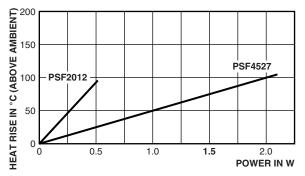
DIMENSIONS in inches (millimeters)



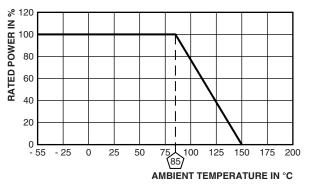
WODEL	–	П		vv	vv 1
PSF2012	0.200 ± 0.020 (5.08 ± 0.508)	0.096 ± 0.015 (2.44 ± 0.381)	0.040 ± 0.010 (1.02 ± 0.254)	0.125 ± 0.005 (3.18 ± 0.127)	$\begin{array}{c} 0.050 \pm 0.005 \\ (1.27 \pm 0.127) \end{array}$
PSF4527	0.455 ± 0.020 (11.56 ± 0.508)	0.167 ± 0.010 (4.24 ± 0.254)	0.100 ± 0.010 (2.54 ± 0.254)	0.275 ± 0.005 (6.98 ± 0.127)	0.215 ± 0.005 (5.46 ± 0.127)

SOLDER PAD DIMENSIONS in inches (millimeters)					
MODEL	А	В	L		
PSF2012	0.085 (2.16)	0.070 (1.78)	0.080 (2.03)		
PSF4527	0.155 (3.94)	0.230 (5.94)	0.205 (5.21)		

THERMAL RESISTANCE



DERATING



MATERIAL SPECIFICATIONS		
Element	Precision deposited nickel chrome alloy with controlled annealing	
Encapsulation	Molded epoxy on the 2012 and molded thermoplastic on the 4527	
Core Fire-cleaned high purity ceramic		
Termination	Termination Standard leadframe material is solder-coated copper on the 2012 and solder-coated bronze on the 4527	

PACKAGING REEL PACKAGING CODE MODEL TAPE WIDTH DIAMETER **PIECES/REEL** LEAD (Pb)-FREE LEAD (Pb)-BEARING PSF2012 12 mm/embossed plastic 330 mm/13" 2000 ΕA ΤA PSF4527 330 mm/13' 1200 ΕA ΤA 24 mm/embossed plastic

Note

Embossed carrier tape per EIA-481. •

Revision: 13-Jul-12

2

PSF

Vishay Dale

TEMPERATURE COEFFICIENT OF RESISTANCE

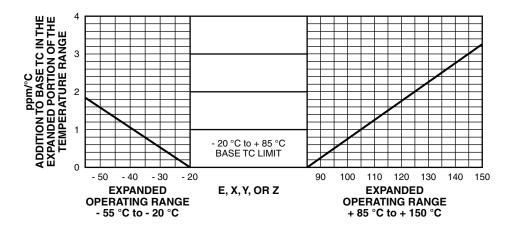
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Temperature coefficient (TC) of resistance is normally stated as the maximum amount of resistance change from the original + 25 °C value as the ambient temperature increases or decreases. This is most commonly expressed in parts per million per degree centigrade (ppm/°C).

The resistance curve over the operating temperature range is usually a non-linear curve within predictable maximum limits. PSF resistors have a very uniform resistance temperature characteristic when measured over the operating range of - 20 °C to + 85 °C. The standard temperature coefficients available are

 $E = \pm 25 \text{ ppm/°C}, X = \pm 15 \text{ ppm/°C}, Y = \pm 10 \text{ ppm/°C}, \text{ and } Z = \pm 5 \text{ ppm/°C}.$

Some applications of the PSF require operation beyond the specifications of -20 °C to +85 °C. The change in temperature coefficient of resistance is very small (less than ± 0.05 ppm/°C) over the expanded temperature range of -55 °C to +150 °C. Therefore, when operating outside the range of -20 °C to +85 °C, the designer can plan for a worst case addition of ± 0.05 ppm/°C for each degree centigrade beyond either -20 °C or +85 °C as indicated in the graph. This applies to all four temperature coefficient codes.



Example:

Assume the operating characteristics demand a temperature range from - 55 °C to + 125 °C. This requires a \pm 35 °C \triangle below - 20 °C and a \pm 40 °C \triangle above + 85 °C. The extreme \triangle being \pm 40 °C means that the worst case addition to the specified TC limit of \pm 0.05 ppm/°C times \pm 40 °C or \pm 2 ppm/°C. Therefore, a Z which is characterized by a base TC limit of \pm 5 ppm/°C over the temperature range of - 20 °C to + 85 °C will exhibit a maximum temperature coefficient of \pm 7 ppm/°C over the expanded portion of the temperature range of - 55 °C to 125 °C.

PERFORMANCE

TEST	CONDITIONS OF TEST	TEST RESULTS (TYPICAL TEST LOTS)		
Life	MIL-STD-202, method 108, 1000 h rated power at + 85 °C	≤ ± 0.04 %		
Short time overload	MIL-PRF-55342, paragraph 4.8.6	≤ ± 0.01 %		
Thermal shock	MIL-STD-202, method 107, - 65 °C to + 150 °C	≤ ± 0.02 %		
Low temperature operation	MIL-PRF-55342, paragraph 4.8.5	≤ ± 0.02 %		
Resistance to bonding exposure	MIL-STD-202, method 210	≤ ± 0.02 %		
Moisture resistance	MIL-PRF-55342, paragraph 4.8.9	≤ ± 0.08 %		
Solder mounting integrity	MIL-PRF-55342, paragraph 4.8.13, 3 kg for 30 s	No evidence of mechanical damage		
Dielectric withstanding voltage	MIL-STD-202, methods 301 and 105	≤ ± 0.01 %		
Solderability	MIL-STD-202, method 208	95 % coverage		



Vishay

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