Vishay Techno

CDMV

www.vishay.com

Thick Film Chip Dividers, Medium Voltage



LINKS TO ADDITIONAL RESOURCES

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3-1	
3D Models	Fo

FEATURES

- Voltage up to 1415 V
- Maximum resistance ratio of 700:1
- Flow solderable
- Tape and reel packaging available
- Termination style: 3-sided wraparound termination or single termination flip chip available
- · Suitable for solderable, epoxy bondable, or wire bondable applications
- Termination material: solder-coated nickel barrier terminations standard; gold, palladium silver, platinum gold, platinum silver, or platinum palladium gold terminations available
- Multiple styles, termination materials, and configurations, allow wide design flexibility
- Epoxy bondable or wire bondable terminations available Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

STANDARD ELECTRICAL SPECIFICATIONS							
GLOBAL MODEL	CASE SIZE	POWER RATING P _{70°C} W	MAXIMUM WORKING VOLTAGE ⁽¹⁾ V	RESISTANCE RANGE ⁽²⁾ Ω	TOLERANCE ⁽³⁾ ± %	TEMPERATURE COEFFICIENT ⁽⁴⁾ (-55 °C to +155 °C) ± ppm/°C	TCR TRACKING ± ppm/°C
CDMV 2512	2512	1	1415	10K to 75M	0.5, 1, 2, 5, 10	100	50 (typical)

Notes

- Continuous working voltage shall be $\sqrt{P \times R}$ or maximum working voltage, whichever is less Resistance values are calibrated at 100 V_{DC}. Calibration at other voltages available upon request (1)
- (2)

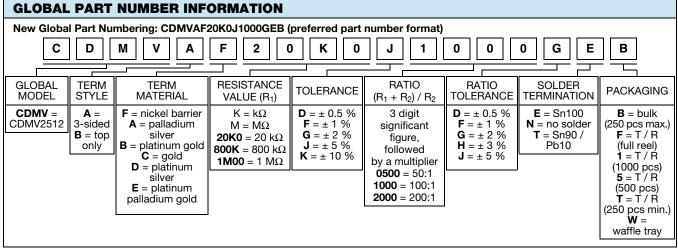
(3) Contact factory for tighter tolerances

(4) Reference only: not for all values specified. Consult factory for your value

VOLTAGE AND TEMPERATURE COEFFICIENTS OF RESISTANCE CHART TYPICAL			
RESISTANCE (Ω)	RATIO (MAXIMUM)	VCR (ppm/V)	TCR (ppm/°C) -55 °C to +155 °C
10K to 100K	200:1	10	150
> 100K to 1M	400:1	10	100
> 1M	700:1	10	100

Note

Contact factory for other ratios



Note

For additional information on packaging, refer to the "Surface Mount Resistor Packaging" document (www.vishay.com/doc?31543)

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HALOGEN FREE

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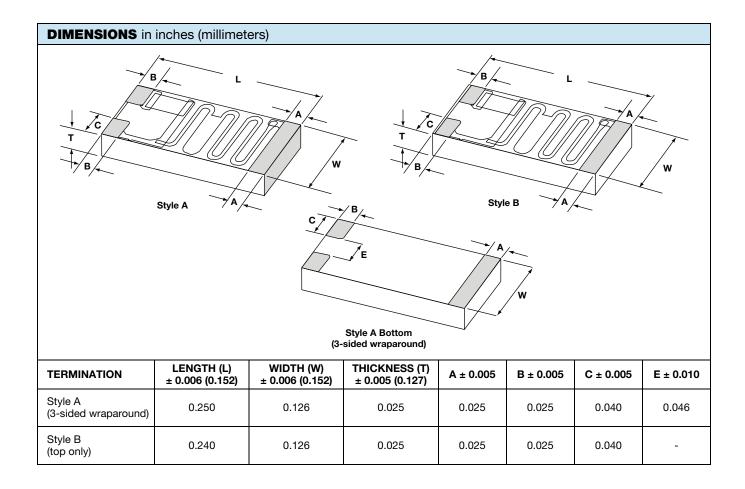
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MATERIAL SPECIFICATIONS			
Resistive element	Ruthenium oxide		
Encapsulation	Ероху		
Substrate	96 % alumina		
Termination	Solder-coated nickel barrier terminations standard. Gold, palladium silver, platinum gold, platinum silver, platinum palladium gold terminations available.		
Solder finish	Pure tin or tin / lead solder alloys standard.		

ENVIRONMENTAL SPECIFICATIONS Operating temperature -55 °C to +155 °C Life Less than 0.5 % change when tested at full rated power

Note

 Reference only: not for all values specified. Consult factory for your size and value

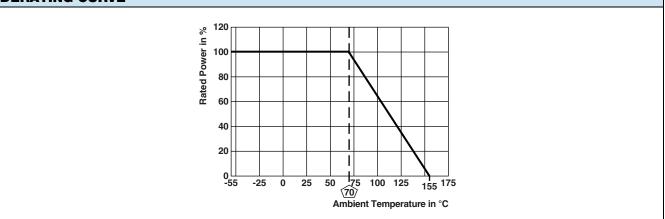




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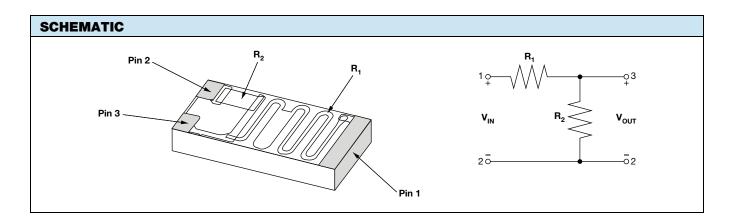
DERATING CURVE



Note

• Reference only: not for all values specified. Consult factory for your specific value

ТҮРЕ	TERMINATION MATERIAL	TERMINATION STYLE	TERMINATION STYLE / MATERIAL CODE	SOLDER TERMINATION CODE	
Solderable	Nickel barrier	3-sided (wraparound)	AF	E or T	
Solderable		Top only (flip chip)	BF		
Epoxy bondable / solderable	Platinum palladium gold	Top only (flip chip)	BE	Ν	
Wire bondable / epoxy bondable	Gold	Top only (flip chip)	BC	Ν	
	Palladium silver		BA		
Epoxy bondable	Platinum gold	Top only (flip chip)	BB	N	
	Platinum silver		BD		





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